

The GaGe Octopus[™] family of multi-channel digitizers features up to 8 channels in a single-slot PCI card with up to 125 MS/s sampling per channel, and up to 4 GB of on-board acquisition memory. Combine several Octopus cards for up to 64 channels in a single system.

With more than 35 new digitizers to choose from, we offer you many more options than ever before.

APPLICATIONS

Radar Design and Test Disk Drive Testing Manufacturing Test Signal Intelligence Lidar Systems Communications Non-Destructive Testing Spectroscopy High-Performance Imaging Ultrasound Test

Octopus CompuScope 82XX

12-Bit Family of Multi-channel Digitizers for the PCI Bus



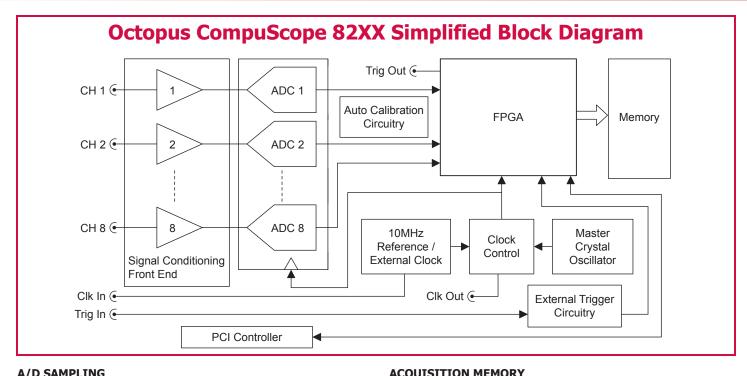
The Octopus family represents a new generation of GaGe digitizers that has all of the advanced features you would expect from a top performance signal capture card:

FEATURES

- 2, 4, or 8 digitizing channels
- 10, 25, 50, 65, 100, or 125 MS/s sampling per channel
- 12 bits vertical resolution
- 128 MS to 2 GS on-board acquisition memory
- More than 100 MHz bandwidth
- Full-size, single-slot PCI card
- Full-featured front-end, with software control over input ranges, coupling and impedances
- 32 bits, 66 MHz PCI standard for 200 MB/s transfer to PC memory
- Ease of integration with External or Reference Clock In and Clock Out, External Trigger In and Trigger Event Out
- Programming-free operation with GageScope[®] oscilloscope software
- Software Development Kits available for LabVIEW, MATLAB, C/C#

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GaGe



A/D SAMPLING

	ACQUISTIC	ON MEMO	DRY			
2, 4 or 8	Active		Total O	n-board I	Memory	
	Channels	128 M				2 G
	1	-		-	-	2 G
		-				
	2	64 M	128 M	256 M	512 M	1 G
	4	32 M	64 M	128 M	256 M	512 M
Channel (product-dependent): 10, 25, 50, 65, 100 or 125 MS/s	8	16 M	32 M	64 M	128 M	256 M
endent):						
125 MS/s, 100 MS/s, 65 MS/s, 50 MS/s, 40 MS/s, 25 MS/s, 20 MS/s, 10 MS/s, 5 MS/s, 2 MS/s, 1 MS/s, 500 kS/s, 200 kS/s, 100 kS/s, 50 kS/s, 20 kS/s, 10 kS/s, 5 kS/s, 2 kS/s, 1 kS/s	Trigger Engines: Source: Input Combination:		CH 1 All co	to 8, EXT	or Softwa	re es logically
SMB						
1 M Ω or 50 Ω ; (software-selectable) AC or DC; (software-selectable) 10 Hz to >100 MHz (see Note 2) DC to >100 MHz (50 Ω see Note 5, slightly less for 1 M Ω)	Slope: Sensitivity:		Posit ±2% This least	Positive or Negative; software-selectab ±2% of Full Scale This implies that signal amplitude must l least 4% of full scale to cause a trigger occur. Smaller signals are rejected as n		
Within ±0.5 dB of ideal response to 90 MHz ±0.5 %	55			point reso		
±100 mV, ±200 mV, ±500 mV, ±1 V, ±2 V, ±5 V, ±10 V (±10 V is only available	Maximum Record Length: Maximum memory depth					
in 1 M Ω)	EXTERNAL T	RIGGER				
\pm 1xFull Range (above \pm 5 V is limited to	Impedance:		2 kΩ	2 kΩ		
±2.5 V)	Amplitude:		Abso	lute maxir	mum ±15 '	V
	Voltage Range	:	±1 V	/, ±5 V (so	oftware-sel	ectable)
	Bandwidth:		>100) MHz		
3-pole Bessel, 1 per channel 24 MHz Individually software-selectable	Coupling: Connector:		AC o SMB			
	12 bits 10.3 bits 64 dB 79 dB 63.6 dB Channel (product-dependent): 10, 25, 50, 65, 100 or 125 MS/s bendent): 125 MS/s, 100 MS/s, 65 MS/s, 50 MS/s, 40 MS/s, 25 MS/s, 20 MS/s, 10 MS/s, 5 MS/s, 2 MS/s, 1 MS/s, 500 kS/s, 200 kS/s, 100 kS/s, 50 kS/s, 20 kS/s, 10 kS/s, 5 kS/s, 2 kS/s, 1 kS/s SMB 1 M Ω or 50 Ω ; (software-selectable) AC or DC; (software-selectable) 10 Hz to >100 MHz (see Note 2) DC to >100 MHz (50 Ω see Note 5, slightly less for 1 M Ω) Within ±0.5 dB of ideal response to 90 MHz ±0.5 % ±100 mV, ±200 mV, ±500 mV, ±1 V, ±2 V, ±5 V, ±10 V (±10 V is only available in 1 M Ω) ± 1xFull Range (above ±5 V is limited to ±2.5 V) 3-pole Bessel, 1 per channel 24 MHz	2, 4 or 812 bits10.3 bits64 dB79 dB63.6 dBChannel (product-dependent):10, 25, 50, 65, 100 or 125 MS/spendent):125 MS/s, 100 MS/s, 65 MS/s, 50 MS/s,40 MS/s, 25 MS/s, 2 0 MS/s, 10 MS/s,5 MS/s, 2 MS/s, 1 MS/s, 500 kS/s,200 kS/s, 100 kS/s, 50 kS/s, 20 kS/s,10 kS/s, 5 kS/s, 2 kS/s, 1 kS/sSMB1 M\Omega or 50 Ω ; (software-selectable)AC or DC; (software-selectable)AC or DC; (software-selectable)10 Hz to >100 MHz (see Note 2)DC to >100 MHz (50 Ω see Note 5,slightly less for 1 M Ω)Within ±0.5 dB of ideal response to 90 MHz±0.5 %±100 mV, ±200 mV, ±500 mV, ±1 V,±2 V, ±5 V, ±10 V (±10 V is only availablein 1 M Ω)± 1xFull Range (above ±5 V is limited to±2.5 V)3-pole Bessel, 1 per channel24 MHz	2, 4 or 812 bits10.3 bits64 dB79 dB63.6 dBChannel (product-dependent):10, 25, 50, 65, 100 or 125 MS/spendent):125 MS/s, 100 MS/s, 65 MS/s, 50 MS/s,40 MS/s, 25 MS/s, 20 MS/s, 10 MS/s,5 MS/s, 2 MS/s, 100 KS/s, 50 kS/s, 20 kS/s,200 kS/s, 100 kS/s, 50 kS/s, 20 kS/s, 100 kS/s, 50 kS/s, 20 kS/s,200 kS/s, 100 kS/s, 50 kS/s, 20 kS/s,1 M Ω or 50 Ω ; (software-selectable)ActiveA criveA d 32 M264 M43-pole Bessel, 1 per channel24 MHz	Active ChannelsIotal O Iotal O12 bits 10.3 bits1128 M256 M64 dB 79 dB1128 M256 M63.6 dB264 M128 MChannel (product-dependent): 10, 25, 50, 65, 100 or 125 MS/s bendent):264 M128 M125 MS/s, 100 MS/s, 65 MS/s, 50 MS/s, 40 MS/s, 25 MS/s, 20 MS/s, 10 MS/s, 5 MS/s, 2 MS/s, 100 KS/s, 50 kS/s, 20 KS/s, 200 kS/s, 100 kS/s, 50 kS/s, 20 kS/s, 100 kS/s, 50 kS/s, 20 kS/s, 10 KS/s, 5 MBTRIGGERINGTRIGERING 10 kS/s, 50 kS/s, 20 kS/s, 200 kS/s, 100 kS/s, 50 kS/s, 20 kS/s, 10 hz to >100 MHz (see Note 2) DC to >100 MHz (soe Note 2) Co >100 MHz (soe Note 2) DC to >100 MHz (soe Note 2) DC t	2, 4 or 812 bits10.3 bits64 dB79 dB63.6 dBChannel (product-dependent):10, 25, 50, 65, 100 or 125 MS/sbendent):125 MS/s, 100 MS/s, 65 MS/s, 50 MS/s,40 MS/s, 25 MS/s, 100 MS/s, 50 MS/s,40 MS/s, 25 MS/s, 00 MS/s, 100 MS/s, 50 MS/s,50 MS/s, 100 MS/s, 50 MS/s, 100 KS/s, 50 MS/s,50 MS/s, 20 MS/s, 100 MS/s, 50 MS/s,50 MS/s, 20 MS/s, 100 KS/s, 50 MS/s,10 KS/s, 50 KS/s, 20 KS/s,10 KS/s, 5 KS/s, 2 KS/s, 1 KS/sSMB1 MQ or 50 Q; (software-selectable)AC or DC; (software-selectable)10 Hz to >100 MHz (see Note 2)DC to >100 MHz (see Note 2)Within ± 0.5 dB of ideal response to 90 MHz $\pm 1.5 \ W$ $\pm 1xFull$ Range (above ± 5 V is limited to $\pm 2.5 \ V$ $\pm 1xFull$ Range (above ± 5 V is limited to $\pm 2.5 \ V$ $\pm 1xFull$ Range (above $\pm 5 \ V$ is limited to $\pm 2.5 \ V$ $\pm 1xFull$ Range (above $\pm 5 \ V$ is limited to $\pm 2.5 \ V$ $\pm 1xFull$ Range (above $\pm 5 \ V$ is limited to $\pm 2.5 \ V$ $\pm 2.5 \ V$ $\pm 1xFull$ Range (above $\pm 5 \ V$ is limited to $\pm 2.5 \ V$ $\pm 1xFull$ Range (above $\pm 5 \ V$ is limited to $\pm 2.5 \ V$ $\pm 1xFull$ Range (above $\pm 5 \ V$ is limited to $\pm 2.5 \ V$ $\pm 1xFull$ Range (above $\pm 5 \ V$ is limited to $\pm 2.5 \ V$ $\pm 1xF$	2, 4 or 812 bits10.3 bits64 dB79 dB63.6 dBChannel (product-dependent):10, 25, 50, 65, 100 or 125 MS/sendent):125 MS/s, 100 MS/s, 65 MS/s, 50 MS/s,40 MS/s, 25 MS/s, 20 MS/s, 10 MS/s,5 MS/s, 2 MS/s, 100 KS/s, 50 kS/s,20 KS/s, 100 KS/s, 50 kS/s,10 kS/s, 55 kS/s, 2 kS/s, 1 kS/s5 MB1 MQ or 50 Q; (software-selectable)AC or DC; (software-selectable)10 Hz to >100 MHz (50 Q see Note 2)DC to >100 MHz (50 Q see Note 5,Sightly less for 1 MQ)Within ±0.5 dB of ideal response to 90 MHz $\pm 10 F W$

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TRIGGER OUT

Impedance: Amplitude: Connector: 50 Ω compatible 0-2.5 V SMB

2 MHz

50 Ω

Rising

SMB

AC

50% ±5%

Minimum 1 V RMS

Maximum 2 V RMS

Minimum 1 V RMS Maximum 2 V RMS

Maximum product sample rate

2 MHz (from External Clock)

1 kHz (from Internal Clock)

±1 ppm (0 to 50°C ambient)

Maximum product sample rate

INTERNAL CLOCK

Accuracy:

EXTERNAL CLOCK

Maximum Frequency: Minimum Frequency: Signal Level:

Termination Impedance: Sampling Edge: Duty Cycle: Connector: Coupling:

EXTERNAL REFERENCE

The External Reference timebase is used to synchronize the Internal Sampling Clock

50 Ω

Rising 50% ±5%

SMB

0-2.5 V

SMB

50 Ω compatible

50% ±10%

Frequency: Signal Level:

Impedance: Sampling Edge: Duty Cycle: Connector:

CLOCK OUT

Maximum Frequency: Minimum Frequency:

Signal Level: Impedance: Duty Cycle: Connector:

MULTIPLE RECORD

Pre-trigger Data: Record Length: Up to virtually full record length 128 points minimum. Can be defined with a 64 points resolution.

10 MHz ±1000 ppm; (software-selectable)

TIMESTAMPING

Resolution: Counter turnover: One sampling interval >24 hours continuous

CARD SIZE

Single-slot, full-length PCI

SYSTEM REQUIREMENTS

PCI-based computer, minimum Pentium II 500 MHz, with at least one free full-length PCI slot, 128 MB RAM, 100 MB hard disk.

COOLING SYSTEM

Minimum CFM Requirement: Characterization in progress

⁺POWER (IN WATTS, PER CARD)

25.0 W (typical)

[†]Measured on a typical 4-channel Octopus card.

PCI BUS INTERFACE

 Plug-&-Play:
 Fully su

 Bus Mastering:
 Fully su

 Scatter-Gather:
 Fully su

 Bus Width:
 32 bits

 Bus Speed:
 66 MHz

 Bus Throughput:
 200 MB (66 MH and nu

 Compatibility:
 PCI-cor Also v.2

Fully supported Fully supported Fully supported 32 bits 66 MHz or 33 MHz 200 MB/s to PC memory (66 MHz PCI; dependent on motherboard and number of PCI-PCI bridges) PCI-compliant, v.2.2 Also v.2.1 systems that supply 3.3 V to PCI slot

MULTI-CARD SYSTEMS

Supported by all Octopus CompuScope models, GageScope, and SDKs.

OPERATING SYSTEMS

Windows Vista, XP:	All Versions
Windows 2000:	SP1 or higher

APPLICATION SOFTWARE

GageScope: Windows-based software for programming-free operation					
LITE Edition:	Included with purchase, provides basic functionality				
Standard Edition:	Provides limited functionality of advanced analysis tools, except for Extended Math				
Professional Edition:	Provides full functionality of all advanced analysis tools				

SOFTWARE DEVELOPMENT KITS (SDK)

CompuScope SDK for C/C# for Windows* CompuScope SDK for MATLAB for Windows CompuScope SDK for LabVIEW for Windows

*C/C# SDK is compatible with LabWindows/CVI 7.0+ compiler. Visual Basic.NET support available with purchase of C/C# SDK.

Contact your GaGe Sales Agent for information on Linux support.

WARRANTY

One year parts and labor Certificate of NIST Traceable Calibration is included.

All specifications subject to change without notice.

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Notes to specifications:

- 1) Measured at 125 MS/s in the ±500 mV range with 50 Ω input impedance using a 10 MHz sine wave with an amplitude of 95% of full scale and the on-board filtering capability.
- 2) 10 Hz at 1 M Ω only.
- 3) Measured at 125 MS/s in the ±500 mV range with 50 Ω input impedance with an amplitude of 95% of full scale.
- 4) Measured on ±500 mV, ±1 V, ±2 V input ranges for both 50 Ω and 1 M Ω input impedance settings.
- 5) Measured on ± 1 V, ± 2 V, ± 5 V input ranges using the 50 Ω input impedance setting.

Unless otherwise specified, all dynamic performance specs have been qualified on engineering boards.

ORDERING INFORMATION

Hardware & Upgrades

naramare a opgrad			
Octopus 12-bit Family	2 Channel	4 Channel	8 Channel
10 MS/s	CS8220: OCT-822-000	CS8240: OCT-824-000	CS8280: OCT-828-000
25 MS/s	CS8222: OCT-822-002	CS8242: OCT-824-002	CS8282: OCT-828-002
50 MS/s	CS8224: OCT-822-004	CS8244: OCT-824-004	CS8284: OCT-828-004
65 MS/s	CS8225: OCT-822-005	CS8245: OCT-824-005	CS8285: OCT-828-005
100 MS/s	CS8227: OCT-822-007	CS8247: OCT-824-007	CS8287: OCT-828-007
125 MS/s	CS8229: OCT-822-009	CS8249: OCT-824-009	CS8289: OCT-828-009
Memory Upgrade: 128 Memory Upgrade: 128 Memory Upgrade: 128 Memory Upgrade: 128	B MS to 512 MS B MS to 1 GS	OCT-181-001 OCT-181-003 OCT-181-005 OCT-181-007	
6" SMB to BNC male 6" SMB to BNC male 5" SMB to BNC female 5" SMB to BNC female 5" SMB to SMB jumpe 5" SMB to SMB jumpe			
6" SMB to SMB jumper cable - 4 pack ACC-001-0. <u>eXpert™ Firmware Options</u> eXpert Signal Averaging Firmware Option 250-181-0 eXpert FIR Filtering Firmware Option 250-181-0 eXpert Peak Detection Firmware Option 250-181-0 eXpert FFT Firmware Option 250-181-0 eXpert FFT Firmware Option 250-181-0 eXpert FFT Firmware Option bundle 888-100-0 (Signal Averaging, FIR Filtering and Peak Detection)			
GageScope® Softwa GageScope: Lite Editio GageScope: Standard with Purchase of CompuScop GageScope: Profession with Purchase of CompuScop	on Edition De Hardware) nal Edition	Included 300-100-351 300-100-354	
Software Developm GaGe SDK Pack on CD CompuScope SDK for CompuScope SDK for CompuScope SDK for) C/C# MATLAB	200-113-000 200-200-101 200-200-102 200-200-103	

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